

(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
02.01.2004 Bulletin 2004/01

(51) Int Cl.7: **G01B 11/00**

(21) Application number: **03013854.9**

(22) Date of filing: **18.06.2003**

(84) Designated Contracting States:
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR
HU IE IT LI LU MC NL PT RO SE SI SK TR
 Designated Extension States:
AL LT LV MK

(72) Inventors:
 • **Lyons, Joseph H.**
Wilton, Connecticut 06897 (US)
 • **Whelan, Joseph G.**
San Francisco, California 91407 (US)

(30) Priority: **28.06.2002 US 391931 P**
22.11.2002 US 301627

(74) Representative: **Grünecker, Kinkeldey,**
Stockmair & Schwanhäusser Anwaltssozietät
Maximilianstrasse 58
80538 München (DE)

(71) Applicant: **ASML Holding, N.V.**
5503 La Veldhoven (NL)

(54) **System and method for automated focus measuring of a lithography tool**

(57) A system and method are used to calibrate a focus portion of an exposure section of a lithography tool. A wafer is exposed so that a resulted or formed patterned image is tilted with respect to the wafer. The tilting can be imposed based on controlling a wafer stage to tilt the wafer or a reticle stage to tilt the reticle. The wafer is developed. Characteristics of the tilted patterned image are measured with a portion of the lithography tool to determine focus parameters of an exposure system. The portion can be an alignment system.

The measuring step can measure band width and/or band location of the tilted patterned image. Sometimes, more than one patterned image is formed on the wafer, then the measuring step can measure distance between bands and shifting of the bands with respect to a central axis of the wafer. The focus parameters can be focus tilt errors and/or focus offset. The focus parameters are used to perform calibration. Calibration is done by either automatically or manually entering compensation values for the measured focus parameters into the lithography tool.

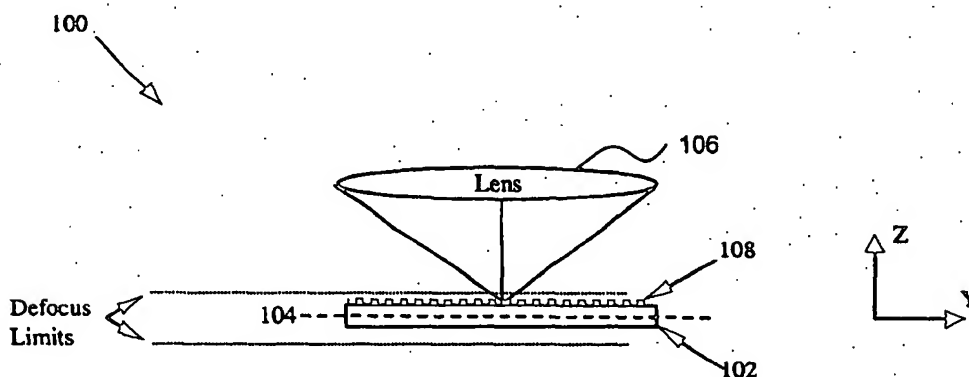


FIG.1

Description

BACKGROUND OF THE INVENTION

Field of the Invention

[0001] The present invention is directed to a system and method for automated measuring of a focus portion of an exposure section of a lithography tool.

Background Art

[0002] Lithography systems are routinely calibrated to ensure the exposure optics are focused properly on a wafer surface. This decreases fabrication of devices that do not comply with certain tolerances by reducing printing or imaging errors caused by focus offset or focus tilt errors. One method of calibrating focus optics involves using a focus test reticle with structures that are patterned into a layer of photoresist on a wafer. The patterned structures are then evaluated manually using a microscope and recorded data is input into a spreadsheet to determine calibration compensation values. These values are used to calibrate the exposure optics. Typically, this method causes several hours of downtime a week while an operator fabricates the test wafer, analyzes it under the microscope, and enters the data. Thus, this method is time consuming, costly in terms of lost production time, and prone to errors due to reliance on human judgement. Other methods require the use of costly external equipment to generate calibration values.

[0003] Therefore, what is needed is a system and method to calibrate lithography tools by automating measurement of focus offset and focus tilt using an existing lithography system (e.g., a stepper alignment system) to evaluate a patterned structure or patterned structures.

BRIEF SUMMARY OF THE INVENTION

[0004] Embodiments of the present invention provide a method of calibrating a lithography tool. The method includes the step of exposing a wafer so that a resultant patterned image is tilted with respect to the wafer. The method also includes the step of developing the wafer. The method also includes the step of measuring characteristics of the tilted patterned image with a portion of the lithography tool to determine focus parameters of an exposure system. The method also includes the step of using the focus parameters to perform the calibrating step.

[0005] Other embodiments of the present invention provide a method including the step of exposing a wafer positioned at an angle so as to create a band-shaped pattern on the wafer that is tilted with respect to the wafer. The method also includes the step of developing the wafer. The method also includes the step of measuring

characteristics of the patterned image with a wafer alignment system. The method also includes the step of determining focus parameters based on the measuring step. The method also includes the step of calibrating a focus portion of an exposure section of a lithography tool based on the determining step.

[0006] Still other embodiments of the present invention provide a method that includes the step of stepping a wafer along a predetermined axis. The method also includes the step of exposing the wafer at two or more points along the predetermined axis so that formed patterned images are tilted with respect to the wafer. The method also includes the step of developing the wafer method also includes the step of measuring characteristics of the patterned images with a wafer alignment system. The method also includes the step of determining focus parameters based on the measuring step. The method also includes the step of calibrating a focus portion of an exposure section of a lithography tool based on the determining step.

[0007] Advantages of the above embodiments are they only require one or more repeat exposures, and then the wafer alignment system of an exposure tool is used to measure the patterned structure or structures as opposed to a human or external equipment. No external measurement is required and opportunity for human error is eliminated. This saves both time and money.

[0008] Another advantage of at least some of the embodiments is that the patterns are printed at a tilt with respect to a wafer, which allows focus conditions during exposure to be monitored. These systems in method save both time and money, and virtually eliminate the measuring errors caused by human judgments.

[0009] Another advantage of at least some of the above embodiments is that by performing multiple exposures, multiple bands are produced, which allows for a Theta X (Tx) measurement as well as a Z measurement in a scanning lithography tool. The method can also be extended to monitor stepper characterization, of Z, Tx and Ty focus axis.

[0010] Further embodiments, features, and advantages of the present inventions, as well as the structure and operation of the various embodiments of the present invention, are described in detail below with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS/FIGURES

[0011] The accompanying drawings, which are incorporated herein and form a part of the specification, illustrate the present invention and, together with the description, further serve to explain the principles of the invention and to enable a person skilled in the pertinent art to make and use the invention.

[0012] FIG 1 shows an initial position of a wafer with respect to a reticle image plane according to embodiments of the present invention.

[0013] FIG. 2A shows a side view of a tilted wafer after being exposed with a band according to embodiments of the present invention.

[0014] FIG. 2B shows a front view of the wafer of FIG. 2A.

[0015] FIG. 3 shows a flow chart of a method for forming a band on a wafer according to embodiments of the present invention.

[0016] FIG. 4A shows a side view of a tilted wafer after being exposed with a band according to embodiments of the present invention.

[0017] FIG. 4B shows a top view of the wafer in FIG. 4A.

[0018] FIG. 5A shows a side view of a tilted wafer after being exposed with a band according to embodiments of the present invention.

[0019] FIG. 5B shows a top view of the wafer in FIG. 5A.

[0020] FIG. 6A shows a side view of a wafer at two exposure positions after being exposed at each position with a band according to embodiments of the present invention.

[0021] FIG. 6B shows a top view of the wafer of FIG. 6A.

[0022] FIG. 6C shows a scanning beam that scans the wafer in FIGs. 6A and 6B.

[0023] FIG. 6D shows a graph generated from scanning using a lithography tool's alignment system beam to determine characteristics of patterns on the wafer in FIGs. 6A, 6B, and 6C according to embodiments of the present invention.

[0024] FIG. 7A shows a side view of a wafer at two exposure positions after being exposed at each position with a band according to embodiments of the present invention.

[0025] FIG. 7B shows a top view of the wafer of FIG. 7A.

[0026] FIG. 8A shows a side view of a wafer at two exposure positions after being exposed at each position with a band according to embodiments of the present invention.

[0027] FIG. 8B shows a top view of the wafer of FIG. 8A.

[0028] FIG. 9 shows a flow chart depicting a calibration method according to embodiments of the present invention.

[0029] FIG. 10 shows a flow chart depicting a calibration method according to embodiments of the present invention.

[0030] FIG. 11 shows a flow chart depicting a calibration method according to embodiments of the present invention.

[0031] The present invention will now be described with reference to the accompanying drawings. In the drawings, like reference numbers indicate identical or functionally similar elements. Additionally, the left-most digit(s) of a reference number identifies the drawing in which the reference number first appears.

DETAILED DESCRIPTION OF THE INVENTION

Overview

5 [0032] According to the present invention, a reticle is populated with identical structures possessing a critical dimension (CD) near a resolution capability of a stepper being used. The structures are densely spaced, covering the entire exposure field. The reticle is exposed such that the image formed by the exposure tool's projection optics is tilted with respect to a wafer. The result of exposing the reticle image onto a tilted wafer is that only a portion of the reticle image will resolve upon the wafer within the usable depth of focus (UDOF) of the exposure tool. Within the UDOF, the structures will print in the photoresist. However, the structures falling outside the UDOF will not adequately resolve and the incident light will be adequate to clear away all of the photoresist. After proceeding through a develop process, a visible band of patterned photoresist will remain on the wafer.

10 [0033] When resist properties and exposure conditions remain constant characteristics of the system can be determined based on a band location and width. The location of the band is primarily a function of the focus offset and tilt. The width of the band is dependent on tilt, UDOF, resist sensitivity, exposure dose, and other factors. The contrast between the patterned band and the bare silicon wafer can be detected by a wafer alignment system. After the development process, the wafer is positioned back into the exposure tool and the precise locations of the patterned bands are established by the wafer alignment system. The locations of the bands can be used to calculate the focus offset and focus tilt errors that were present during the exposure step. The methods can also be extended to monitor stepper characterization, of Z, Tx and Ty focus errors.

15 [0034] FIG. 1 shows a portion 100 of an exposure section of a lithography tool according to embodiments of the present invention. FIG. 1 shows a side view of a field of view (e.g., a field or a surface) on a wafer 102 where an image plane 104 formed by projection optics 106 is properly focused. In this configuration, an image 108 prints properly over an entire length of a slot (not shown) of a reticle (not shown) onto substantially an entire length of the field.

20 [0035] As described below, a tilted grating monitoring method according to embodiments of the present invention relies on a basic principle of lithography that small features will not print and resolve when printed out of focus. This principle applies whether the defocus is positive or negative. Normally, wafer 102 is oriented to be in plane with image plane 104. For the purposes of discussion, an example employing a lithographic scanner will be described. However the same technique may be extended to analyze focus performance on any other lithography tool.

25 [0036] It is to be appreciated, exposing of a multiple test band or band on a wafer either in plane or tilted with

respect to a focal plane of the focus optics can be performed and used for other lithography reasons. For example, to view a field on a wafer and to determine various characteristics and/or parameters of the field or the wafer for many different lithographic related reasons.

Calibrating a Focus Portion Based on Measuring Focus Parameters with One Patterned Band

[0037] FIGs. 2A and 2B show a wafer 200 according to embodiments of the present invention. Wafer 200 is purposely tilted so that areas 202 of the field of wafer 200 that are out of focus have no printed features. The presence of printed features only in a center of the field of the wafer 200 results in a band 204. Thus, areas 202 have no features or resolved or printed because they are out of focus. As shown by orienting axes in FIGs. 2A and 2B, an X-axis runs into the page in FIG. 2A, as does the scanned exposure that runs along the X-axis. Introducing a tilt about the X-axis allows a center 206 of the field to remain in focus, while the top and bottom edges (e.g., areas 202) of the field go out of focus. Given a limited depth of focus, fine features do not resolve well at the top and bottom (e.g., areas 202) of the printed field.

[0038] FIG. 3 shows a flow chart depicting a tilted grating monitoring method 300 according to embodiments of the present invention. At step 302, a wafer (e.g., wafer 200) is tilted Theta X (Θ_x) (shown in FIG. 2A) from a nominal image plane (e.g., plane 104) in an exposure section. At step 304, a grating reticle (e.g., a reticle filled with fine features) is positioned to expose the wafer. At step 306, a field (e.g., a surface of wafer 200) is exposed during scanning of the wafer along the X-axis. The tilt causes the top and bottom regions (e.g., areas 202) of each field to be printed out of focus. This results in the small lines of the grating not resolving at the top and bottom (e.g., areas 202) regions of the field when the resist (e.g., positive resist) in these regions of the field get cleared away. After clearing away these regions, a band (e.g., band 204) remains in a center portion (e.g., portion 206) of the field. At step 308, a development process is performed on the wafer to clear resist from regions (e.g., areas 202) where the image did not print due to defocus.

[0039] With reference again to FIGs. 2A and 2B, the characteristic of printed band 204 allows for some characterization of focus performance. For example, how precisely band 204 is centered along a Y-axis of the field is dependent on how well a focus system (not shown) established Z while exposing. Also, how precisely width W of band 204 is generated is related to how well the focus systems control Theta-X. Thus, by measuring these characteristics of band 204 with an alignment systems metrology system, compensation values are determined and used to calibrate the exposure optics.

[0040] FIGs. 4A and 4B show a wafer 400 according to embodiments of the present invention. In these fig-

ures, wafer 400 is created with the same Theta-X tilt as FIGs. 2A and 2B, but a Z focus offset is introduced. This change moves a printed band 402 away from a central X-axis. The Z focus offset has shifted the in-focus region from a center 404 to an edge 406 of the field. The relationship between band position and the Z offset is a function of the induced Theta-X tilt.

[0041] FIGs. 5A and 5B show a wafer 500 according to embodiments of the present invention. Wafer 500 has been exposed with Theta X offset from nominal, which affects a width W of band 502. A vertical width W of printed band 502 can also be affected by process parameters, such as dose, resist thickness, and resists choice among others. For example, width W1 may be a nominal width and width W2 may be a width caused by the above factors. This makes it difficult to establish a scale factor and nominal readings for Theta-X to band size. To overcome this difficulty, the embodiments discussed below expose and measure multiple bands, with two bands used merely as an example embodiment.

Calibrating a Focus Portion Based on Measuring Focus Parameters with Multiple Patterned Bands

[0042] FIGs. 6A and 6B show two exposure positions for a wafer 600 according to embodiments of the present invention. Wafer 600 includes two pattern areas or bands 602 and 604. Band 602 is formed when wafer 600 is positioned at Z offset 2 and band 604 is formed when wafer 600 is positioned at Z offset 1. FIG. 6C shows a scanning beam 606, which can come from an alignment system in the lithography tool, that scans along a Y axis to detect bands 602 and 604. FIG. 6D shows a graph generated from detection of reflected light from wafer 600. As is shown, bands 602 and 604 cause higher levels of reflected intensity 608 and 610, respectively. This can be preferably based on reflectivity effects of light, although scattering effects can also be detected. The reflectivity and/or scattering effects of light are based on the light hitting a rough surface (e.g., bands 602 and 604) compared to just reflecting from a smooth or relatively smoother surface (e.g., unpatterned areas of wafer 600).

[0043] FIGs. 7A and 7B show two exposure positions for a wafer 700 according to embodiments of the present invention. Wafer 700 includes two pattern areas or bands 702 and 704. Band 702 is formed when wafer 700 is positioned at Z offset 2 and band 704 is formed when wafer 700 is positioned at Z offset 1. The wafer position for wafer 600 in FIGs. 6A and 6B is shown in phantom in FIG. 7A. By moving wafer 700 a distance Z shift from a position of wafer 600, bands 702 and 704 are shifted upwards Y1 change and Y2 change, respectively, on wafer 700.

[0044] FIGs. 8A and 8B show two exposure positions for a wafer 800 according to embodiments of the present invention. Wafer 800 includes two pattern areas or bands 802 and 804. Band 802 is formed when wafer 800

is positioned at Z offset 2 and band 804 is formed when wafer 800 is positioned at Z offset 1. The wafer position for wafer 600 in FIGs. 6A and 6B is shown in phantom. Thus, by rotating wafer 800 a distance Tx-shift from a position of wafer 600, bands 802 and 804 are shifted upwards Y1-change and downward Y2-change, respectively, on wafer 800.

[0045] FIGs. 6-8 show the use of a double tilted grating technique that helps to uncouple Z and Theta-X from the issues that affect band size, as discussed above with respect to single band wafers. Although these embodiments are shown with two bands, it is to be appreciated any number of parallel bands can be formed and measured without departing from the scope of the invention. In embodiments showing the double tilted grating method, the field is exposed twice. The same induced Theta-X tilt is used for both exposures. However, a different Z offset is introduced on each exposure in order to move the resulting grating to the top and bottom of the field. This results in two bands being printed. Thus, the double band tilted grating test uses a Z focus offset in combination with a known Theta X tilt to print a set of grating bands that is offset from the center of the field. In the embodiments shown, each field is exposed twice, once with a positive Z offset and one with a negative Z offset. The result is two bands, one in the top half of the field and one in the bottom half of the field.

[0046] In some embodiments, the following formulas are used to determine Z and Theta X. It is to be appreciated, other known formulas for determining these values can also be used. The location of the center of each band in the field (such as shown in FIG. 6B) is referred to as Y Position, which can be defined by the following formula:

$$Y_{\text{Position}} = \frac{Z_{\text{Offset}}}{\sin(\text{ThetaX})}$$

For small angles, $\sin(\theta) \approx \theta$ so the equation simplifies to:

$$Y_{\text{Position}} = \frac{Z_{\text{Offset}}}{\text{ThetaX}}$$

If system focus errors are incorporated, the equation becomes:

$$Y_{\text{Position}} = \frac{Z_{\text{Offset}} + \Delta Z}{\text{ThetaX} + \Delta \text{Tx}}$$

Where ΔZ and ΔTx are the system focus errors. Creating an equation for each of the two bands results in:

$$Y_1 = \frac{Z_1 + \Delta Z}{\text{Tx} + \Delta \text{Tx}} \quad Y_2 = \frac{Z_2 + \Delta Z}{\text{Tx} + \Delta \text{Tx}}$$

Notice that the same system focus errors and Tx offset apply to both bands. Solving the first equation for ΔZ

and the second for ΔTx gives:

$$\Delta Z = Y_1(\text{Tx} + \Delta \text{Tx}) - Z_1 \quad \Delta \text{Tx} = \frac{Z_2 + \Delta Z}{Y_2} - \text{Tx}$$

Substituting one into the other and solving gives us our final equations for the system focus errors:

$$\Delta Z = \frac{Y_1 \cdot Z_2 - Y_2 \cdot Z_1}{Y_2 - Y_1} \quad \Delta \text{Tx} = \frac{Z_2 - Z_1}{Y_2 - Y_1} - \text{Tx}$$

In these formulas, the delta Z and Tx system focus errors are not affected by the width of the bands. Therefore, they are independent of process effects, to a first order approximation.

[0047] In some embodiments, the location of the bands is determined with a native alignment system in a lithographic exposure tool. As discussed above, by scanning an illumination beam (FIG. 6C) along the Y axis and measuring the diffraction efficiency or reflectivity of the wafer surface, the locations of the bands can be measured (FIG. 6D). In this fashion, one can expose a double tilted grating, develop the wafer, and have it scanned by the lithographic exposure tool to assess one or more focus performance factors in a relatively quick fashion. Then, these factors can be easily used to generate compensation values to calibrate the lithography tool, as discussed above.

Methods of Calibrating Focus

[0048] FIG. 9 shows a flow chart depicting a method 900 according to embodiments of the present invention. At step 902, a wafer is exposed so that a patterned image is tilted with respect to the wafer. In a preferred embodiment, the tilting can be imposed based on controlling a wafer stage to tilt the wafer. Other embodiments include controlling the reticle stage to tilt the reticle. At step 904, the wafer is developed. At step 906, characteristics of the tilted patterned image are measured with a portion of the lithography tool to determine focus parameters of an exposure system. The measuring step can measure band width and/or band location of the tilted patterned image. The focus parameters can be focus tilt errors and/or focus offset, for example. At step 908, the focus parameters are used to perform calibration. Calibration is done by either automatically or manually entering compensation values for the measured focus parameters into the lithography tool.

[0049] FIG. 10 shows a flow chart depicting a method 1000 according to embodiments of the present invention. At step 1002, a wafer is exposed so that a patterned image is tilted with respect to the wafer. The tilting can be imposed based on controlling a reticle stage, for example. At step 1004, the wafer is developed. At step 1006, characteristics of the patterned image are meas-

ured with a wafer alignment system. The measuring step can measure band width and/or band location of the tilted patterned image. At step 1008, focus parameters are determined based on the measuring step. The focus parameters can be focus tilt errors and/or focus offset, for example. At step 1010, a focus portion of an exposure section of a lithography tool is calibrated based on the determining step. Calibration is done by either automatically or manually entering compensation values for the measured focus parameters into the lithography tool.

[0050] FIG. 11 shows a flow chart depicting a method 1100 according to embodiments of the present invention. At step 1102, a wafer is stepped along a predetermined axis. At step 1104, the wafer is exposed at two or more points along the predetermined axis so that patterned images are tilted with respect to the wafer. The tilting can be imposed based on controlling a reticle stage, for example. At step 1106, the wafer is developed. At step 1108, characteristics of the patterned images are measured with a wafer alignment system. The measuring step can measure band width and/or band location of the tilted patterned image. At step 1110, focus parameters are measured based on said measuring step. The focus parameters can be focus tilt errors and/or focus offset, for example. At step 1112, a focus portion of an exposure section of a lithography tool is calibrated based on said determining step. Calibration is done by either automatically or manually entering compensation values for the measured focus parameters into the lithography tool.

Conclusion

[0051] While various embodiments of the present invention have been described above, it should be understood that they have been presented by way of example only, and not limitation. It will be apparent to persons skilled in the relevant art that various changes in form and detail can be made therein without departing from the spirit and scope of the invention. Thus, the breadth and scope of the present invention should not be limited by any of the above-described exemplary embodiments, but should be defined only in accordance with the following claims and their equivalents.

Claims

1. A method of calibrating a lithography tool, the method comprising the steps of:

exposing a wafer so that a resultant patterned image is tilted with respect to the wafer;
developing the exposed wafer;
measuring characteristics of the tilted patterned image with a portion of the lithography tool to determine focus parameters of an expo-

sure system; and
using the focus parameters to perform calibration of the lithography tool.

2. The method of claim 1, further comprising the step of determining focus tilt errors of the exposure system based on said measuring step.
3. The method of claim 1, further comprising the step of determining focus offset of the exposure system based on said measuring step.
4. The method of claim 1, wherein said measuring step comprises the step of measuring a band location of the tilted patterned image.
5. The method of claim 1, further comprising the steps of determining focus offset and focus tilt errors of the exposure system based on said measuring step.
6. The method of claim 1, further comprising the step of tilting a reticle stage holding the reticle to impose the tilt.
7. The method of claim 1, further comprising the step of moving the wafer along a predetermined axis during said exposing step to form at least one additional tilted patterned image on the wafer.
8. The method of claim 7, further comprising the step of determining focus tilt errors of the exposure system based on said measuring step.
9. The method of claim 7, further comprising the step of determining a focus offset of the exposure system based on said measuring step.
10. The method of claim 7, wherein said measuring step comprises the step of measuring a distance between the tilted patterned images.
11. The method of claim 7, wherein said measuring step comprises the step of measuring a distance of a center axis of the tilted patterned images from a center axis of the wafer.
12. The method of claim 7, further comprising the step of determining focus offset and focus tilt errors of the exposure system based on said measuring step.
13. The method of claim 7, wherein said measuring step comprises the steps of:
 - measuring a distance between the tilted patterned images; and
 - measuring a distance of a center axis of the tilt-

ed patterned images from a center axis of the wafer.

14. A method comprising the steps of:

exposing a wafer positioned at an angle so as to create a band-shaped etch pattern on the wafer that is tilted with respect to the wafer; developing the exposed wafer; measuring characteristics of the patterned image with a wafer alignment system; determining focus parameters based on said measuring step; and calibrating a focus portion of an exposure section of a lithography tool based on said determining step.

15. The method of claim 14, wherein said determining step comprises the step of determining focus tilt errors of the focus portion.

16. The method of claim 14, wherein said determining step comprises the step of determining focus offset of the focus portion.

17. The method of claim 14, wherein said measuring step comprises the step of measuring a band location of the tilted patterned image.

18. The method of claim 14, wherein said determining step comprises the steps of:

determining focus offset of the focus portion; and determining focus tilt errors of the focus portion.

19. The method of claim 14, further comprising the step of tilting a reticle stage holding the reticle to impose the tilt.

20. A method comprising the steps of:

stepping a wafer along a predetermined axis exposing the wafer at more than one point along the predetermined axis so that formed patterned images are tilted with respect to the wafer; developing the wafer; measuring characteristics of the patterned images with a wafer alignment system; determining focus parameters based on said measuring step; and calibrating a focus portion of an exposure section of a lithography tool based on said determining step.

21. The method of claim 20, wherein said determining step comprises the step of determining focus tilt er-

rors of the focus portion.

22. The method of claim 20, wherein said determining step comprises the step of determining focus offset of the focus portion.

23. The method of claim 20, wherein said measuring step comprises the step of measuring a distance between the tilted patterned images.

24. The method of claim 20, wherein said measuring step comprises the step of measuring a distance of a center axis of the tilted patterned images from a center axis of the wafer.

25. The method of claim 20, wherein said determining step comprises the steps of determining focus offset and focus tilt errors of the focus portion.

26. The method of claim 20, wherein said measuring step comprises the steps of:

measuring a distance between the tilted patterned images; and measuring a distance of a center axis of the tilted patterned images from a center axis of the wafer.

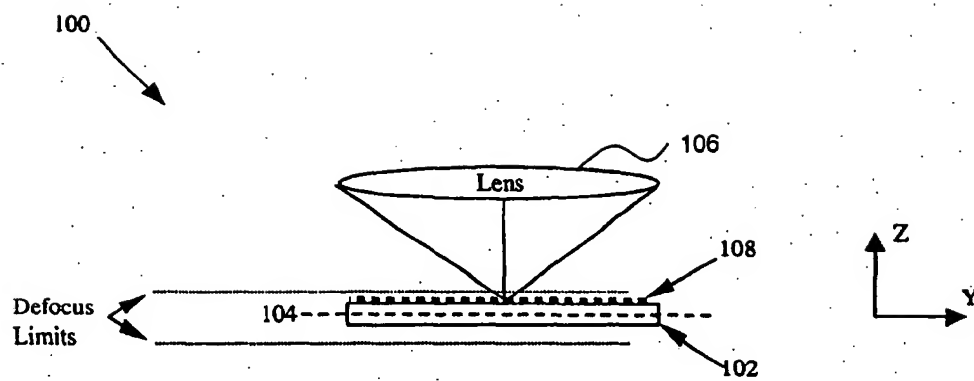


FIG.1

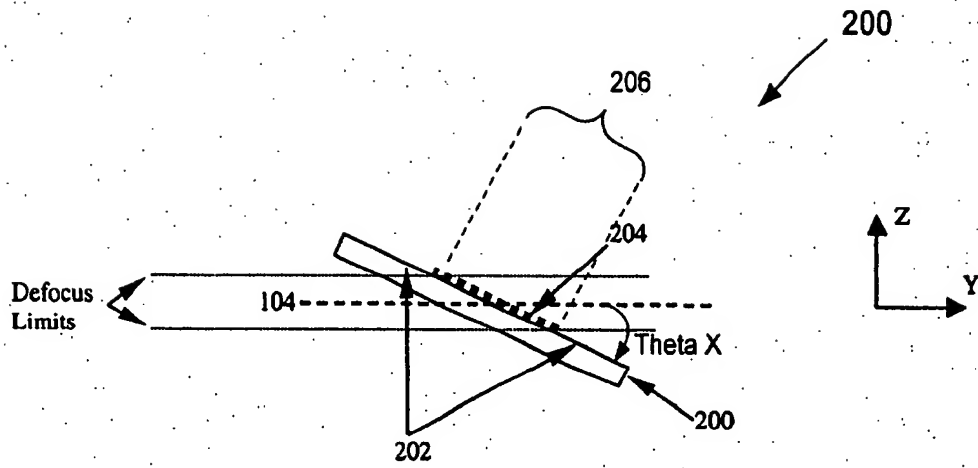


FIG. 2A

200

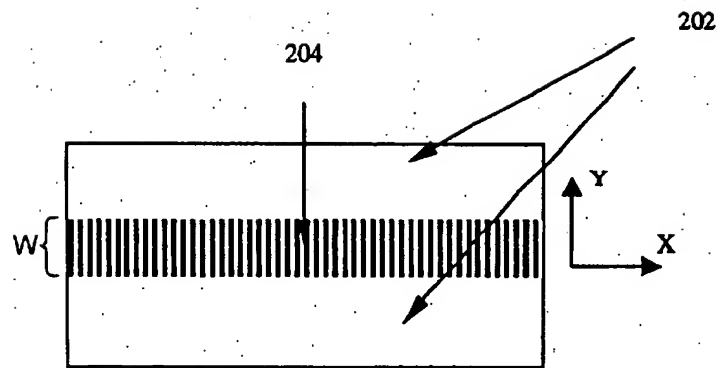


FIG. 2B

300

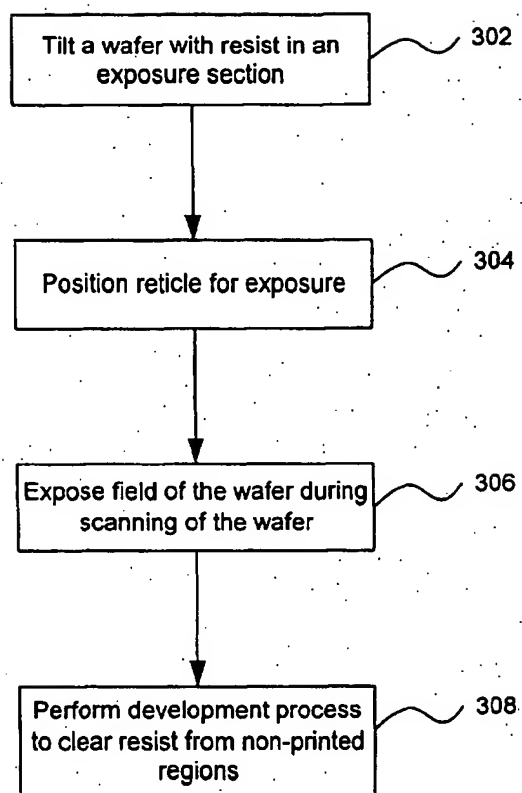


FIG. 3

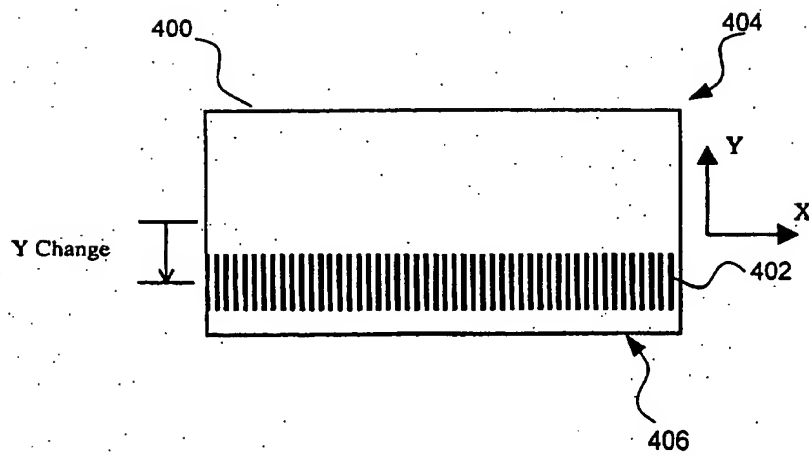
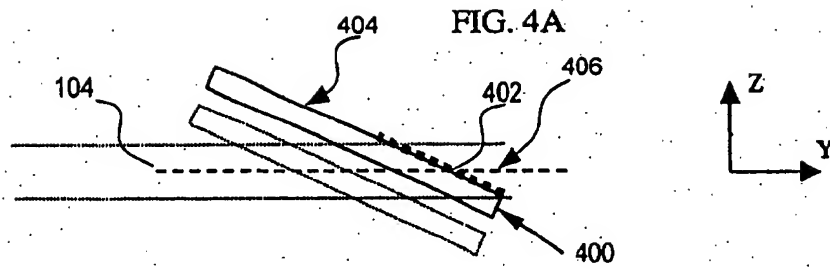


FIG. 5A

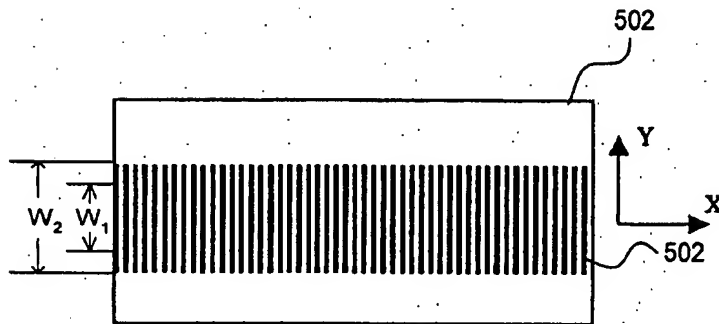
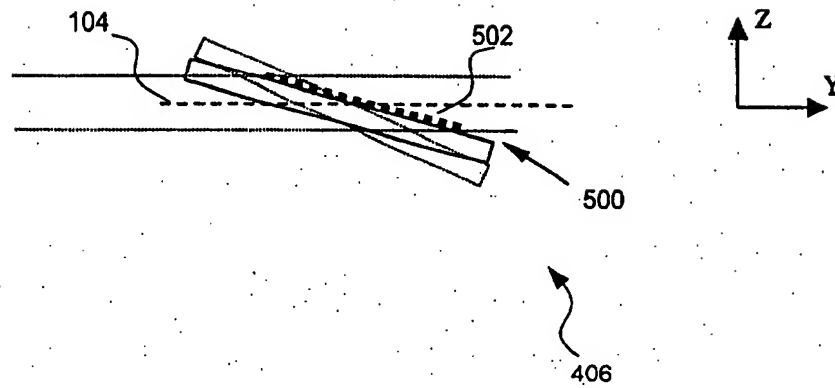


FIG. 5B

FIG. 6B

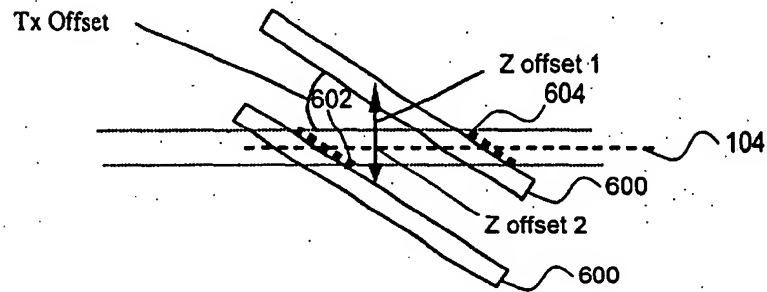
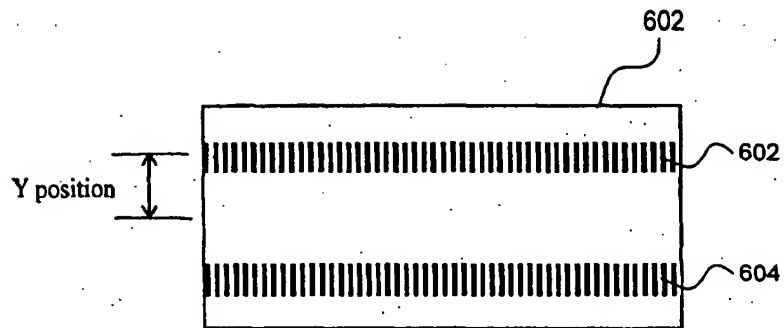


FIG. 6A

FIG 6D

FIG 6C

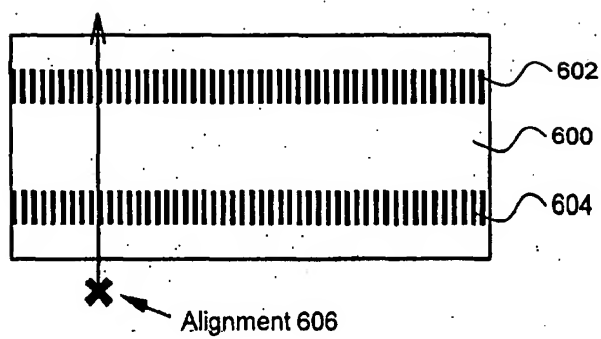
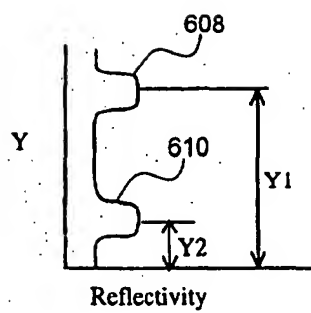


FIG. 7B

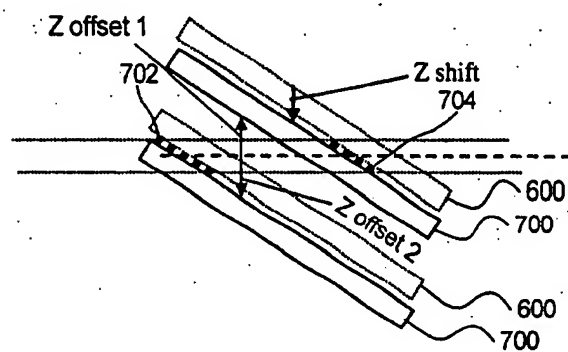
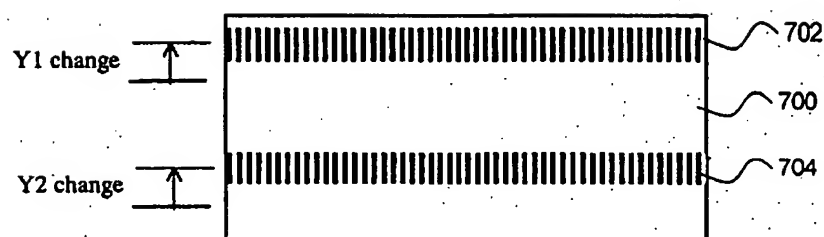


FIG. 7A

FIG. 8B

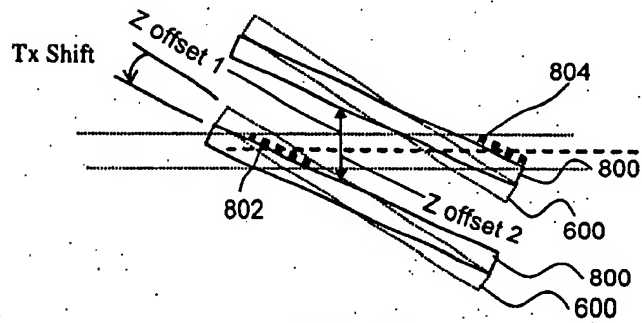
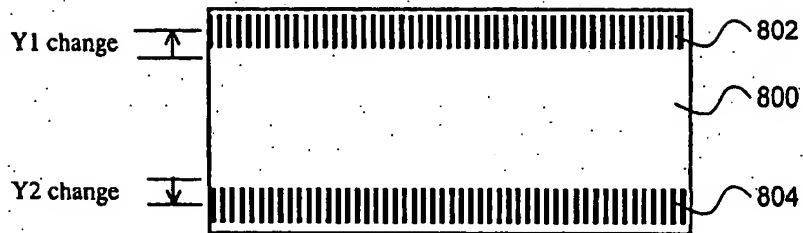
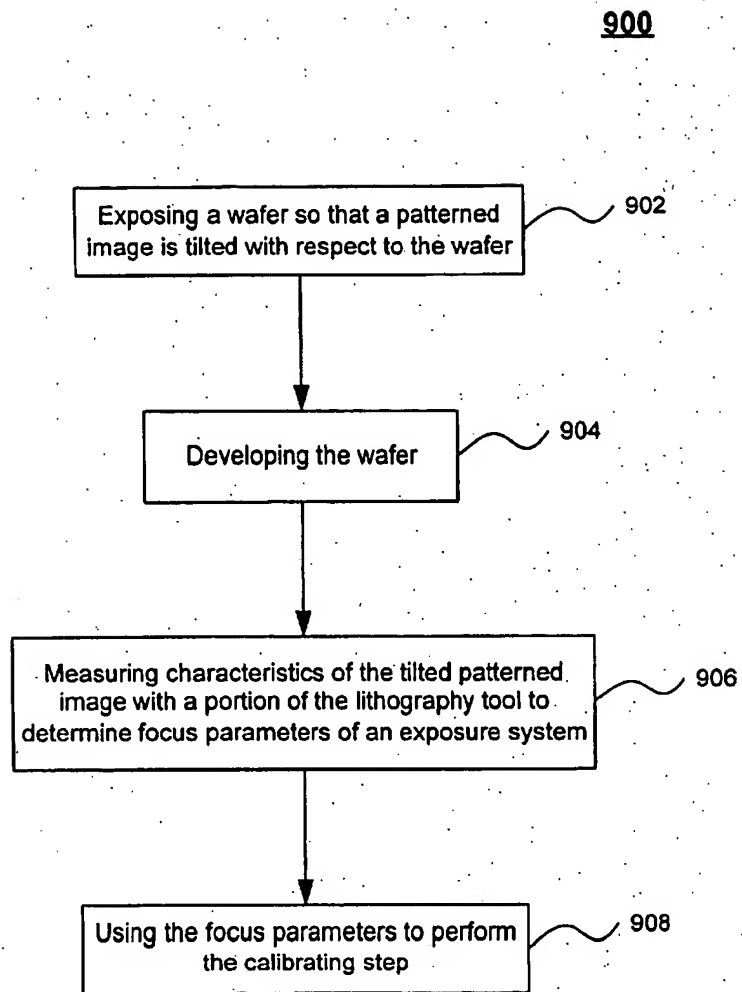


FIG. 8A

**FIG. 9**

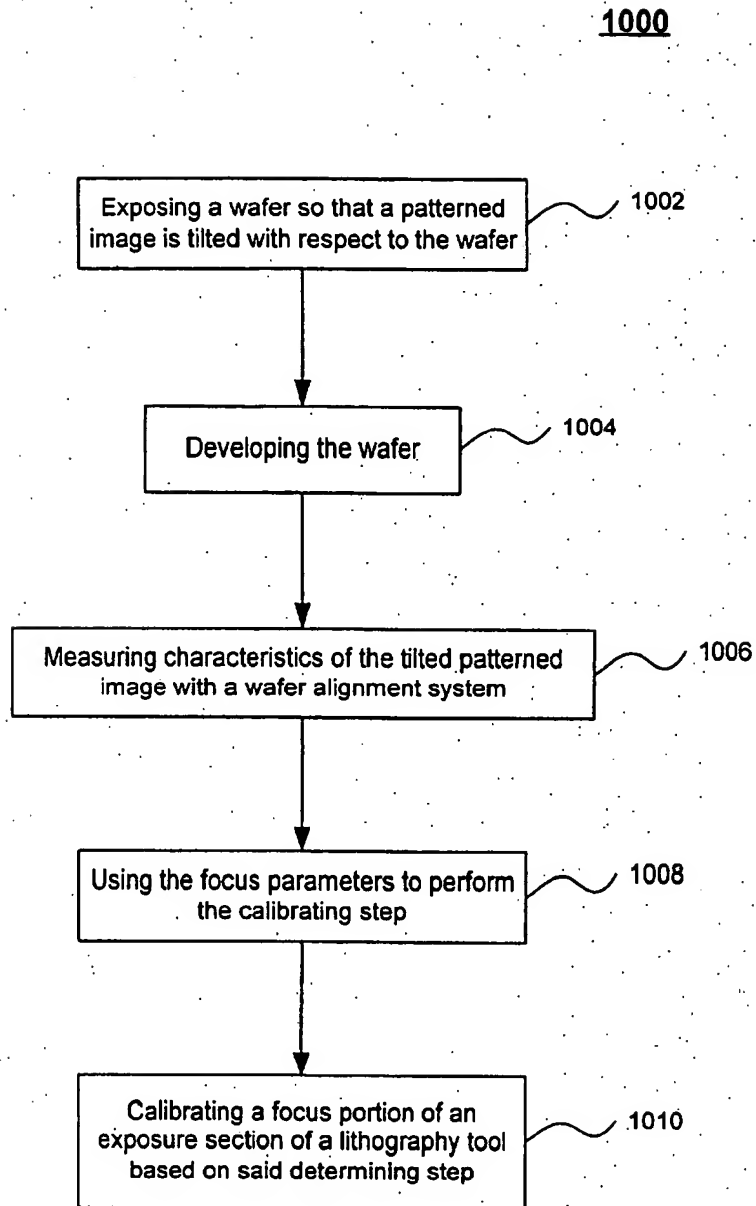
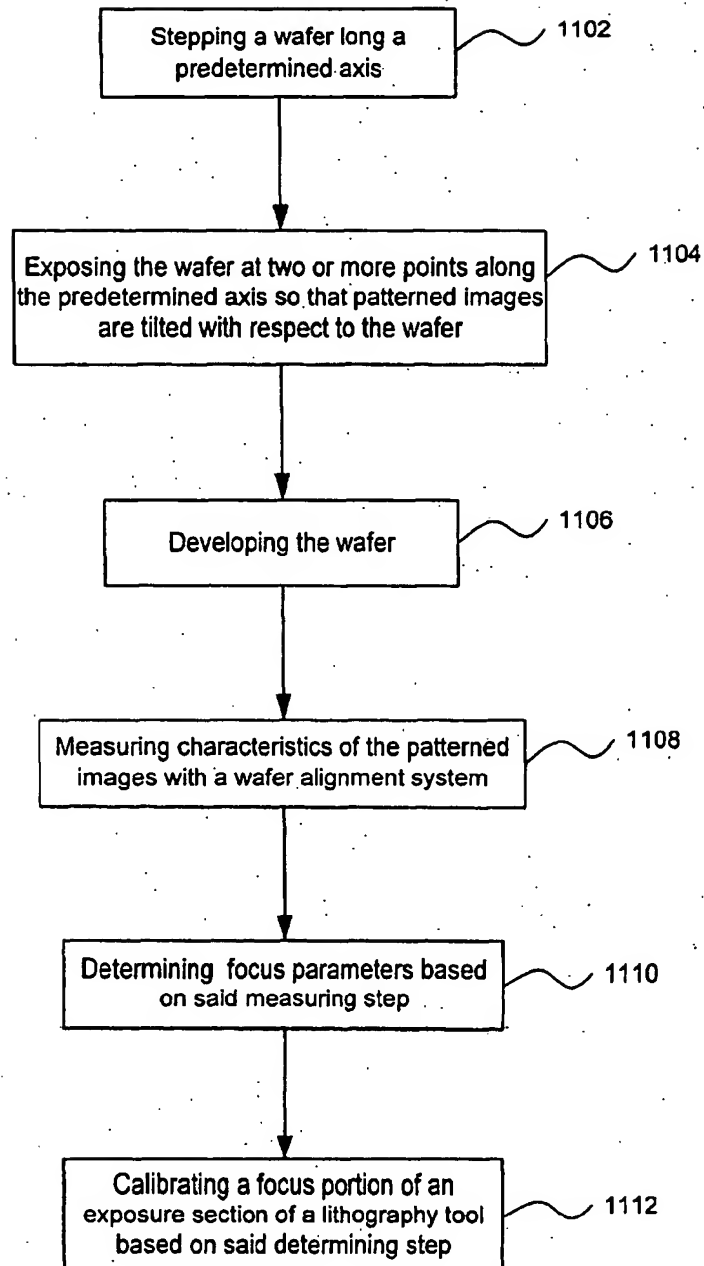


FIG. 10

1100**FIG. 11**